

Application No. 10/023,652

REMARKS

Claims 1-15 are pending in this application. By this Amendment, claims 14 and 15 are amended. Claims 1-13 are allowed. No new matter is added.

Applicant appreciates the allowance of claims 1-13. Applicant submits that claims 14 and 15 are amended to incorporate the "unity gain buffer" feature stated to be an allowable feature in the Examiner's reasons for allowance.

I. Claim Rejection Under 35 U.S.C. §102

Claims 14 and 15 are rejected under 35 U.S.C. §102(e) as anticipated by U.S. Patent No. 6,229,506 to Dawson et al. ("Dawson"). The rejection is respectfully traverse.

Applicant asserts that Dawson does not disclose each and every feature recited in amended claims 14 and 15. For example, Dawson does not disclose a driving method for a display device that includes a pixel driving circuit having a pixel element, the method comprising *inter alia* providing a unity gain buffer having an input and an output and during the programming stage, supplying to the input of the unity gain buffer a voltage at one of a source and drain of a transistor that controls a current supplied to the pixel element. Accordingly, Applicant respectfully request the rejection of claims 14 and 15 under 35 U.S.C. §102(e) be withdrawn. As Dawson is silent regarding a unity gain buffer, Applicant respectfully request the rejection be withdrawn.

II. Conclusion

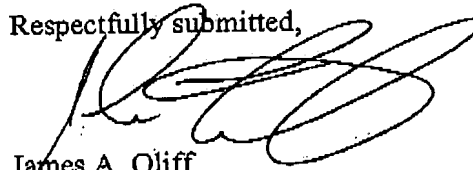
In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims are earnestly solicited.

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Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,


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Date: November 12, 2003

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